Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

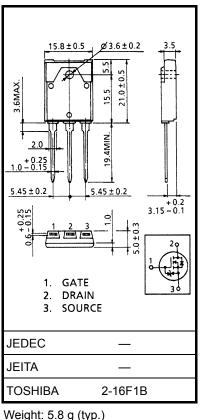
# 2SK3017

DC–DC Converter, Relay Drive and Motor Drive Applications

- Low drain-source ON resistance  $: RDS (ON) = 1.05 \Omega (typ.)$
- High forward transfer admittance  $: |Y_{fs}| = 7.0 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 720 \ V)$
- Enhancement mode :  $V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	900	V
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	900	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	۱ <sub>D</sub>	8.5	А
	Pulse (Note 1)	I <sub>DP</sub>	25.5	А
Drain power dissipation	n (Tc = 25°C)	PD	90	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	966	mJ
Avalanche current		I <sub>AR</sub>	8.5	А
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	9	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C



vveight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	1.39	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	41.6	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 24.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 8.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

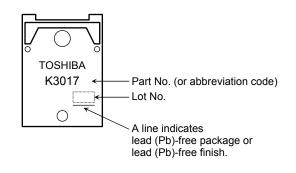
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V		—	±10	μA
Gate-source br	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A		1.05	1.25	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A	3.5	7.0	_	S
Input capacitance	ce	C <sub>iss</sub>			2150		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		35		pF
Output capacitance		C <sub>oss</sub>			220		
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{}_{0 \text{ V}} \int_{\mathcal{V}} \int_{V$	_	25	_	- ns
	Turn-on time	t <sub>on</sub>		_	60	_	
	Fall time	t <sub>f</sub>		_	25	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> = 10 µs	_	120	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	70	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A		37	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			33	_	

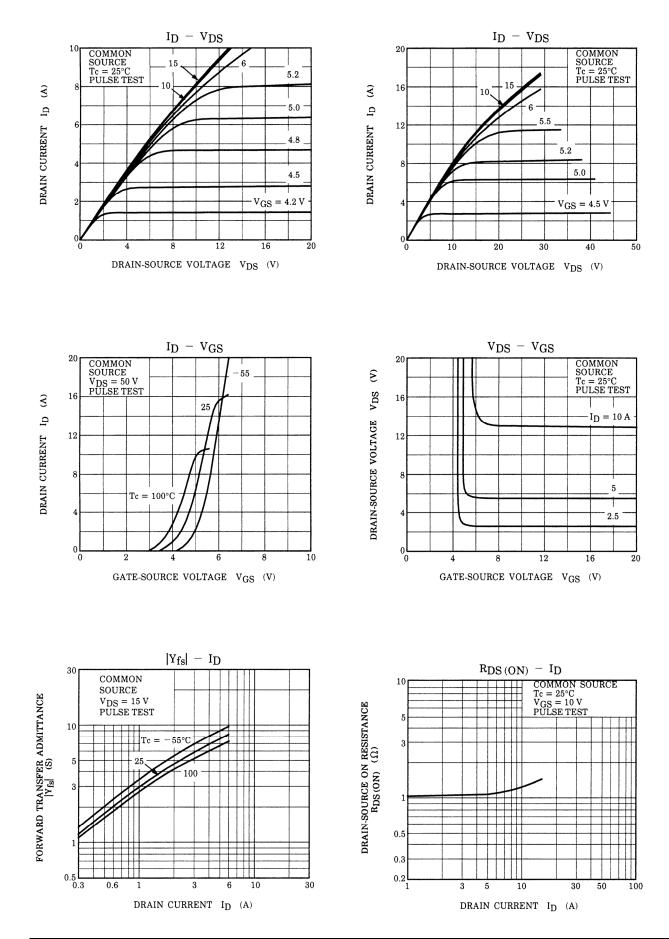
## Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	8.5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—		_	25.5	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 8.5 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 8.5 A, V <sub>GS</sub> = 0 V	_	1300		ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> / dt = 100 Å / µs		14.5		μC

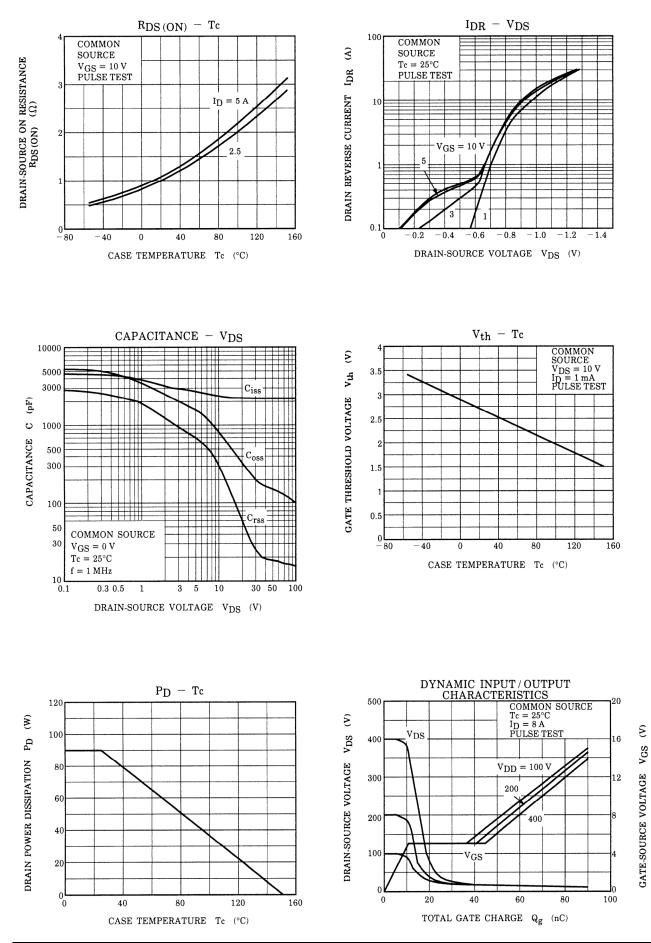
## Marking

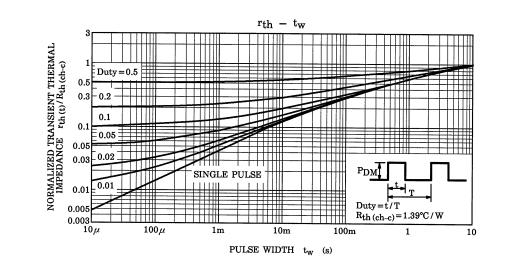


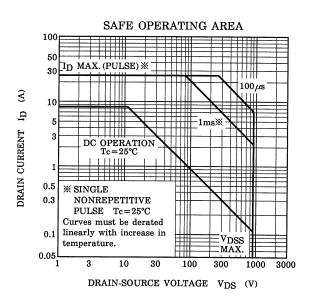
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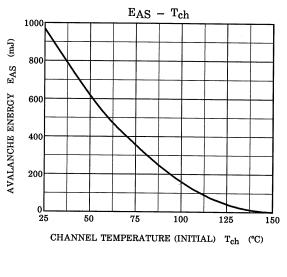


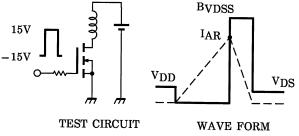
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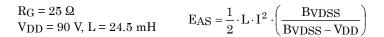












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